

AMENDMENTS TO THE SPECIFICATION:

Please replace the title on page 1, line 1, with the following rewritten title:

--Transceiver Interface Reduction--.

Please replace the paragraph beginning at page 1, line 19, with the following rewritten paragraph:

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--The USB standard has become very popular with manufacturers of personal computers in particular. More recently and particularly with the expansion of the Internet, it is becoming even more necessary for a computer to be connected, at least part of the time, to a telephone line to permit the transfer of data such as electronic mail. In the case of a laptop computer, it is often not convenient or possible to connect to a fixed telephone line. Thus, interfaces have been developed to allow a mobile telephone to be connected to a computer. Such interfaces can place severe demands on a lightweight battery-powered device such as a mobile telephone where considerations of size and complexity of circuitry together with power consumption are very important.--

Please insert the following title on page 1, between lines 29 and 30:

B2
--SUMMARY OF THE INVENTION--.

Please delete the title on page 2, line 1.

Please replace the paragraph beginning at page 2, line 3, with the following rewritten paragraph:

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--Thus, according to the invention, there is provided a transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from ~~said the~~ cable, and a logic circuit connected to the input having a single logic output line, wherein the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level.--

Please replace the paragraph beginning at page 2, line 10, with the following rewritten paragraph:

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--Preferably, the transceiver operates under the USB protocol in which case the interface may form part of a USB device which receives a pair of differential signals as inputs such that the first output state is indicative of a single ended zero state ~~whilst~~ while the second output state is indicative of any other state of ~~said the~~ differential signals. Conveniently, the logic means may be a NOR gate. The transceiver may form part of a USB device which may further include an application specific integrated circuit (ASIC) having a physical connection to ~~said the~~ logic means via ~~said the~~ logic line to facilitate the provision of the USB control protocol. The device may be included in a mobile telephone handset or the like to permit connection to a USB host or hub connected thereto. Where the device is included in a mobile telephone, the USB control functions ~~might~~ may be added to the ASIC providing the telephone functions.--

Please replace the paragraph beginning at page 3, line 8, with the following rewritten paragraph:

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--In the following, a full description of the USB standard is omitted, it being understood that this forms part of the general knowledge of the skilled addressee. Referring to Figures 1 to 4, the table set out in Figure 1 highlights a requirement of the USB standard, namely that a host 1 must be able to determine the speed with which a device 2 connected to the host 1 is communicating via a USB cable 3. This state information is present as a differential signals carried on a pair of twisted pair conductors D+/D- 4,5 housed within a shielded cable 3 which also contains power and ground conductors 7,8. Thus, a single ended zero state (Se0) is indicated if the voltage on both twisted pair conductors 4,5 is low; low speed operation of the device 2 is indicated by a low voltage on D+ 4 and a high voltage on D- 5; full speed operation of the device 2 is indicated by a high voltage on D+ 4 and a low voltage on D- 5; and a bus error is indicated by a high voltage on both D+ and D- 4,5.--

Please replace the paragraph beginning at page 3, line 25, with the following rewritten paragraph:

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--Referring in particular to Figure 4, a typical prior art USB device mode interface 9 has connections 10,11 to the twisted pair conductors D+/D- 4,5. In a receive mode, differential signals carried by the twisted pair cables of the USB cable 3 are received at the connections 10,11 and passed to an amplifier 12 which provides a logic output RCV 13 which is indicative of whether differential data is being received high (RCV high) or ~~not~~ low (RCV low) by the interface 9. In addition

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to the RCV output 13, differential signals received from the twisted pair conductors D+/D- 4,5 are translated by separate Schmitt triggers 14,15 into single ended logic output V_p and V_m . 16,17. Thus, the single ended output 16,17 can be used subsequently to determine in which of the four states set out in Figure 1 the USB is operating.--

Please replace the paragraph beginning at page 4, line 5, with the following rewritten paragraph:

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-- Turning to Figure 5, an embodiment of a USB device mode interface according to the invention is shown in which the RCV output 13 is determined in the same manner as described above in relation to the prior art circuit arrangement. However, the single ended circuit elements 14,15 of the prior art circuit are replaced by a single NOR gate 18 which receives as inputs 19,20 the differential signals D+ and D-14,15. Thus, the gate 18 provides a logic output Se0 21 that is high if both D+ and D-14,15 are low ~~whilst~~ while any other combination of differential input signals results in the Se0 logic output 21 being low. Thus, the interface is capable of determining the presence of the sole state relevant to a device under this portion of the USB standard.--

Please replace the paragraph beginning at page 4, line 16, with the following rewritten paragraph:

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-- It will be ~~recognised~~ recognized by those skilled in the art that a device transceiver interface 9 does not operate in isolation but is required to permit the USB

protocol to be employed by a logic device 2 when communicating with a USB host 1. As mentioned previously, details of the USB signaling protocol may be found in the USB reference – Universal Serial Bus Specification Revision 1.1. Typically, the instructions necessary to implement the protocol are held on an application specific integrated circuit ASIC, which communicates with the transceiver 9 via a set of tracks on a substrate (not shown) on which both components are mounted. Figure 6 shows in outline a mobile telephone handset 22 containing baseband and radio frequency (RF) portions 23,24. The baseband portion further contains an ASIC 25 which, amongst other functions, is provided with the functionality necessary to facilitate the operation of the handset 22 as a USB device when connected to a USB host 1. In this regard, it should be noted that the number of tracks required to connect the ASIC 25 to the USB interface 9 is reduced by one as a single Se0 line replaces the Vp and Vm lines of the prior art interface. Furthermore, as a consequence of the reduction in the number of lines, both the ASIC 25 and the interface 9 require one less pin-out. In a non-illustrated variant of the above embodiment, an ASIC having the conventional number of pin-outs is connected to the USB interface. Whereas, in the prior art case where the Vp and Vm lines from an interface are connected to respective pins on the ASIC, one pin only on the ASIC is connected via the track to the USB interface 9 the other pin being tied in a high logic state.--

Please replace the paragraph beginning at page 5, line 7, with the following rewritten paragraph:

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--Finally, those skilled in the art will ~~recognise~~recognize that the present invention is applicable not only to mobile telephone handsets but to any other device which can operate as a USB device such as printers, scanners and tape drives, for example. It will also be appreciated that the reference throughout the description to a USB host should be taken to include a hub whether self or bus powered.--
